

Challenges for Embedded Deduction

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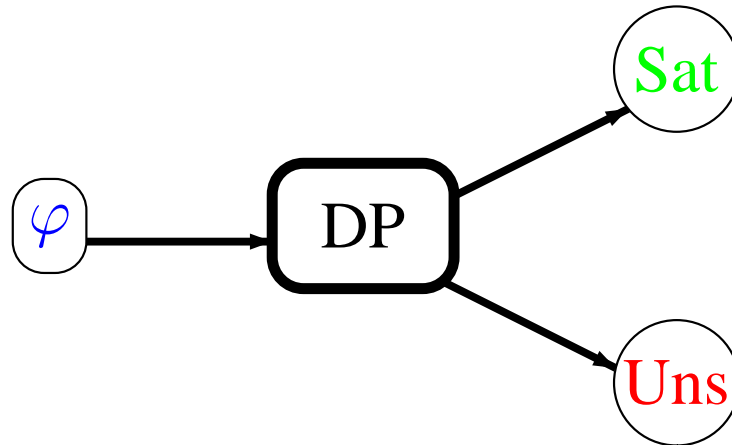
Embedded Deduction

- Most uses of deductive engines are **embedded**.
Simulators, abstractors, typecheckers, compilers, constraint solvers, model checkers, test case generators, synthesizers, test-case generators, fault tree analyzers, ...
- Emphasis in embedded deduction is on **in-the-loop exploration** and **debugging**.
 - Does **this** follow from **that**?
 - Suppose I **retract this** and **assert that**, does it still follow?
- Embedded deduction requires rich interfaces.

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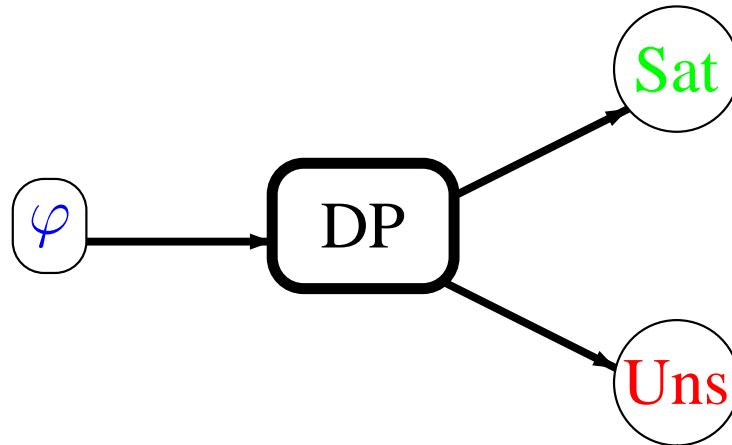
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- Embedded deduction requires rich interfaces.
“The black box nature of the decision procedure is frequently destroyed by the need to integrate it” (Boyer/Moore)

Interfaces for Embedded Deduction



- **Online.** Incremental processing of assertions and queries.
- **Resettable.** Saving, backtracking, and switching contexts.
- **Queryable.** Allow expressions to be simplified wrt a context.
- **Evidential.** Proof objects, unsatisfiable cores, and models.
- **Reliable Automation.** Prompt or even any-time response.
- **Integrable.** Fine-grained integration with other tools.

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ICS (ics.csl.sri.com) tries to provide such a rich interface while keeping overhead small.

Composition in ICS

Open Inference Systems. (Ganzinger, Shankar, R.; 2004)

- **Configurations**

I	DP(<i>T</i>)
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 - Shared blackboard *I* consisting of shared constraints.
 - Theory-specific part *DP(T)* that is like a private notebook.
- **Composition operator** yields DP for union of theories.

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Lazy Integration of DP and SAT.

- **Interface J.** (Abstraction, Lemmas, Assignments)
- **Composition.**

$$\begin{array}{|c|c|} \hline J & DP \\ \hline \end{array} \otimes \begin{array}{|c|c|} \hline J & SAT \\ \hline \end{array} \rightsquigarrow \begin{array}{|c|c|} \hline J & DP, SAT \\ \hline \end{array}$$

- **Optimizations.** (unsatisfiable cores, online integration)

Summary

Where are we?

- Around 10000 – 30000 theorems a second for DP.
- Problems with 10000s of literals using lazy DP/SAT.
- Rapid progress due to healthy competition and improved benchmarking, but “good” benchmark sets still not available.

What's next?

- 100 – 1000 fold speed improvement over next 3 year.
- Construction and collection of better benchmark sets.
- Enough raw speed for most routine embedded applications.

Challenges!

- Practical challenges involve designing interfaces that allow flexible use without loss of efficiency.
- Designing integration architectures that mediate fine-grained interaction between inference components.